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XA-10436 PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

NISHIZAWA, Hirotaka et al.

Intl. Appln. No.: PCT/JP2004/013929

Intl. Filing Date: 24 September 2004

For: SEMICONDUCTOR DEVICE

## INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Attn: Mail Stop PCT, DO/EO/US

Sir:

Pursuant to 37 C.F.R. § 1.56, and without any assertion as to materiality or prior art effect, the documents listed on the attached Form PTO-1449 are hereby cited.

The document on the attached List was cited in the specification, on pages 1-2, and its relevance is indicated therein.

Respectfully submitted

Reg. No. 31,568

MWS:sjk

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September 23, 2005

10/5 50 5 7 8 FORM PTO-1449 Atty. Docket No. INFORMATION DISCLOSURE STATEMENT XA-10436 LIST OF DOCUMENTS CITED BY APPLICANT Applicant NISHIZAWA, Hirotaka et al. Filing Date Group 2887 HEREWITH U.S. PATENT DOCUMENTS Name Sub-Filing Date Document Number Date Class class Initial AΑ AB AC AD FOREIGN PATENT DOCUMENTS Country Class Sub-Translation Examiner Document Number Date Initial class 2000-99215 04/07/00 Yes /EL/ AE Japan AF AG AH ΑI AJ AK AL AM AN AO AP ΑO AR AS AT OTHER (including author, title, date, pertinent pages, etc.) AU Date Considered 09/30/2008 Examiner /Edwyn Labaze/

EXAMINER: Initial it reterence considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.